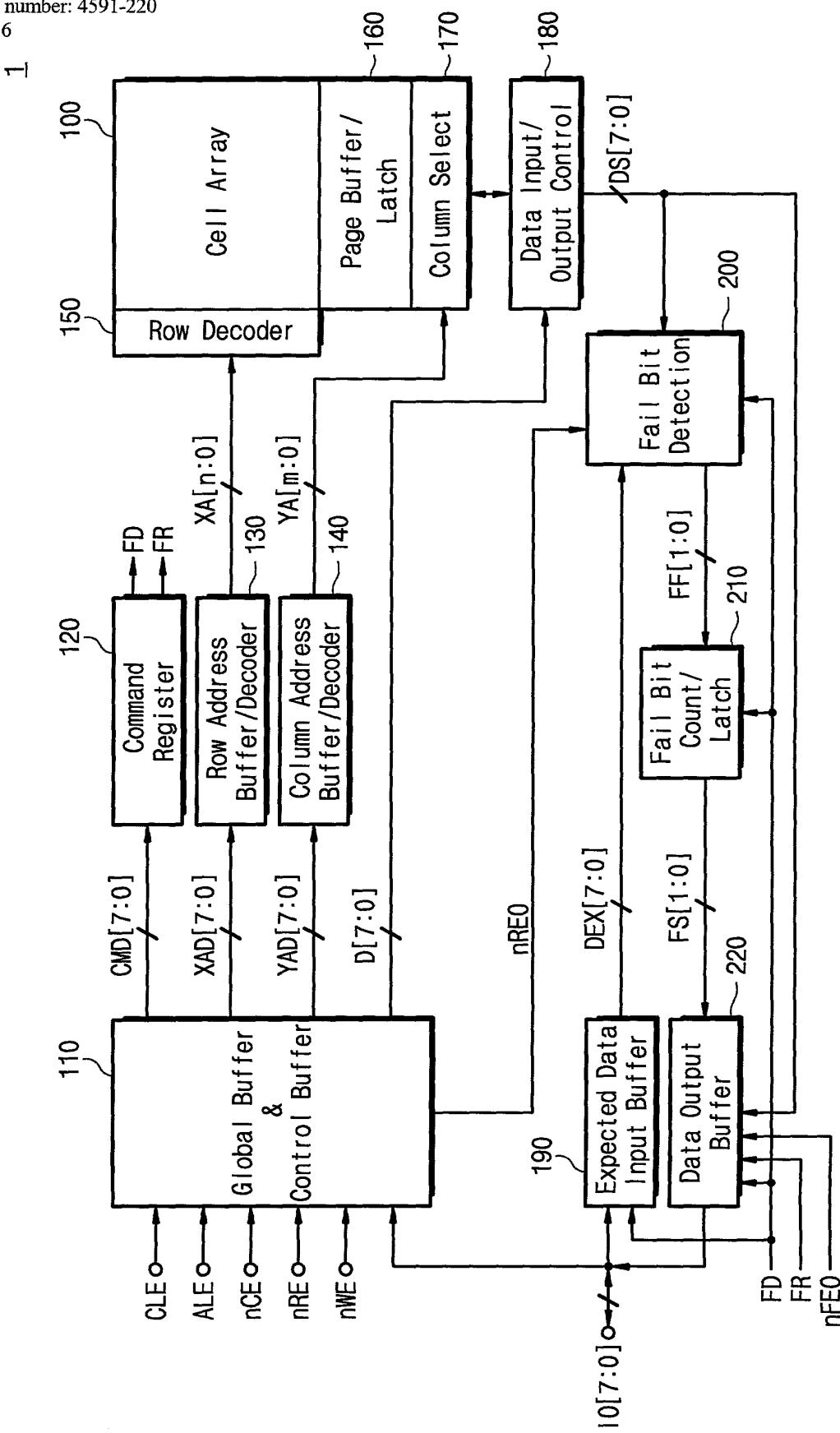


Fig. 1



Inventor: Jae-Yong JEONG and Sung-Soo LEE
Title: NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE WITH A FAIL BIT DETECTING SCHEME AND
METHOD OF COUNTING THE NUMBER OF FAIL
Our docket number: 4591-220
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Fig. 2

190

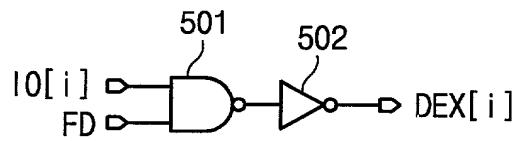


Fig. 3

200

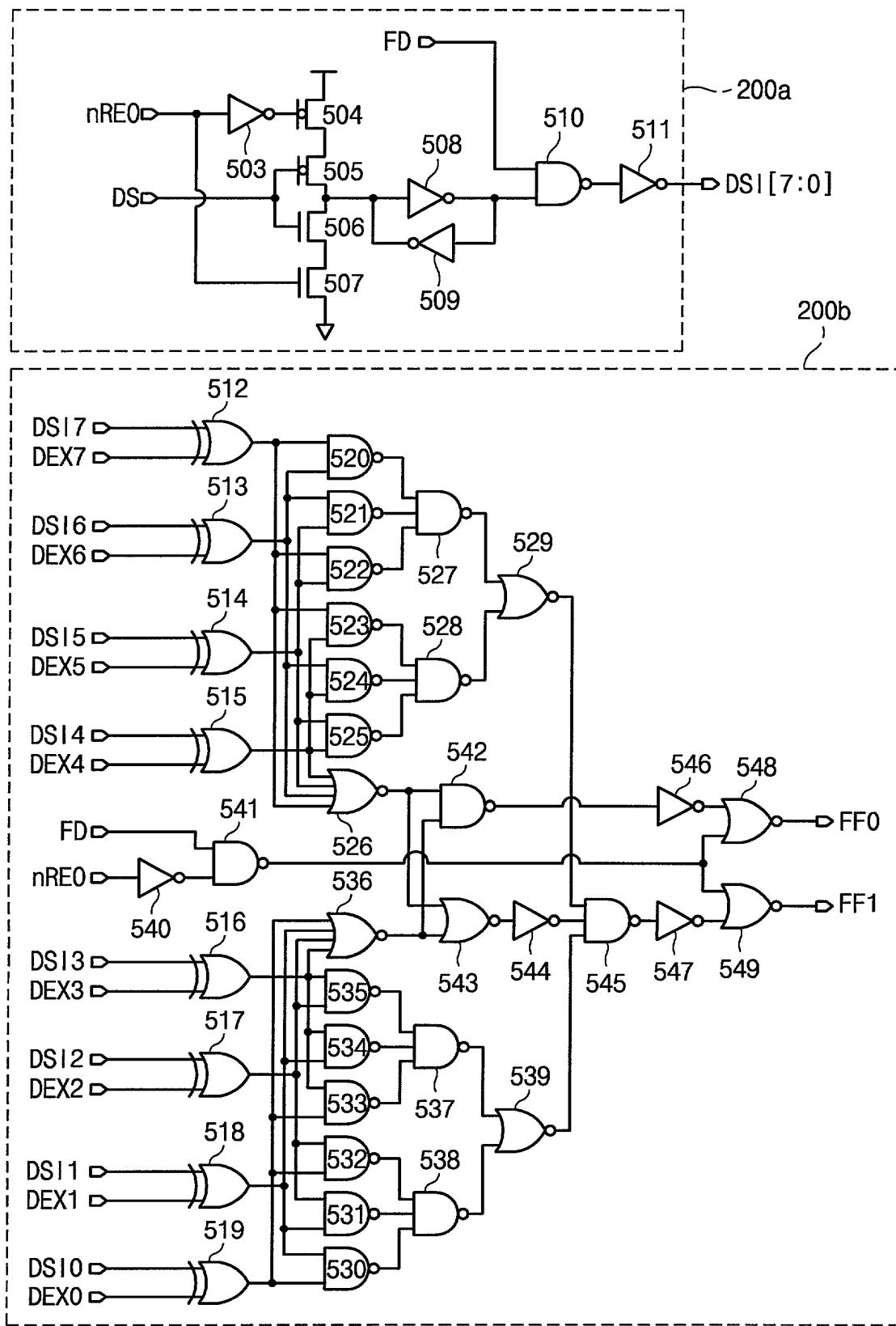


Fig. 4

210

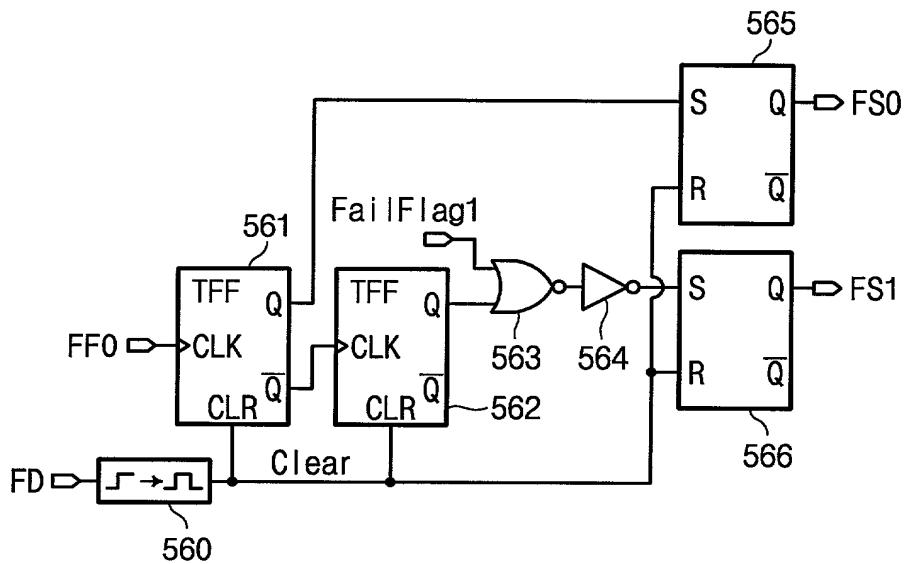


Fig. 5

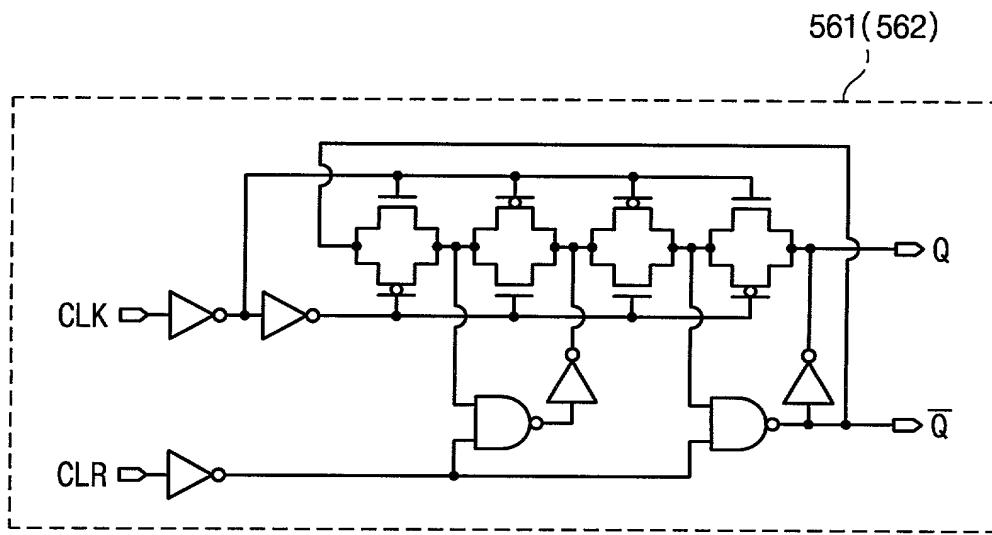


Fig. 6

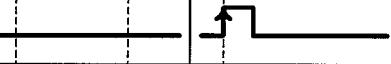
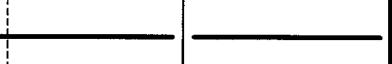
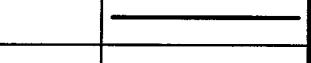
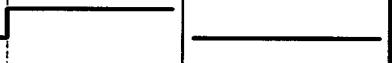
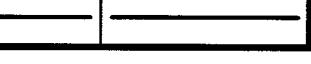
Number of Fail Bits		More than 2 Bits	1 Bit	none
Input	FailFlag0			
	FailFlag1			
Output	FailStatus0			
	FailStatus1			

Fig. 7

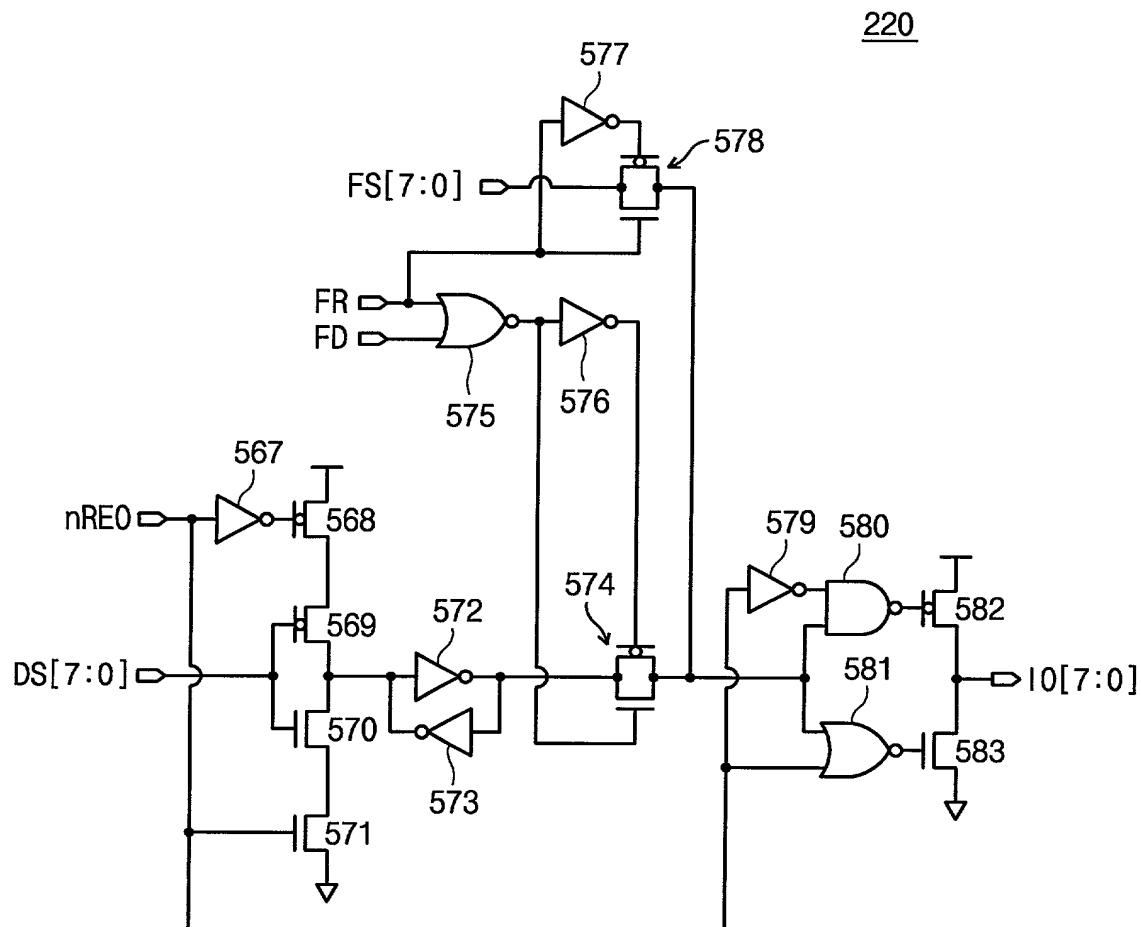


Fig. 8

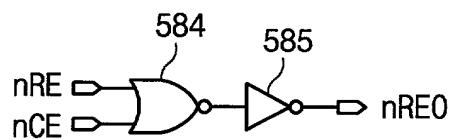


Fig. 9

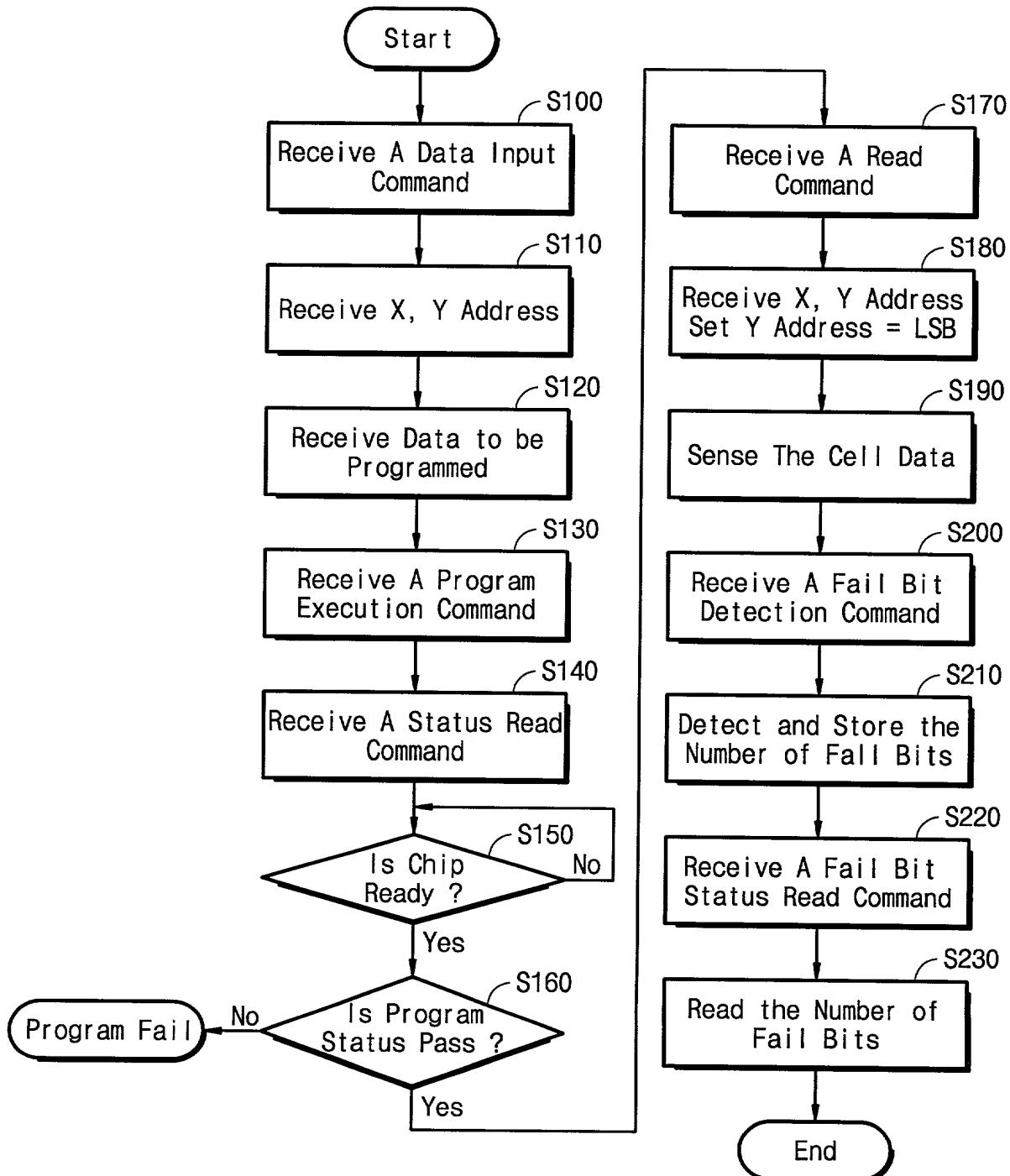


Fig. 10

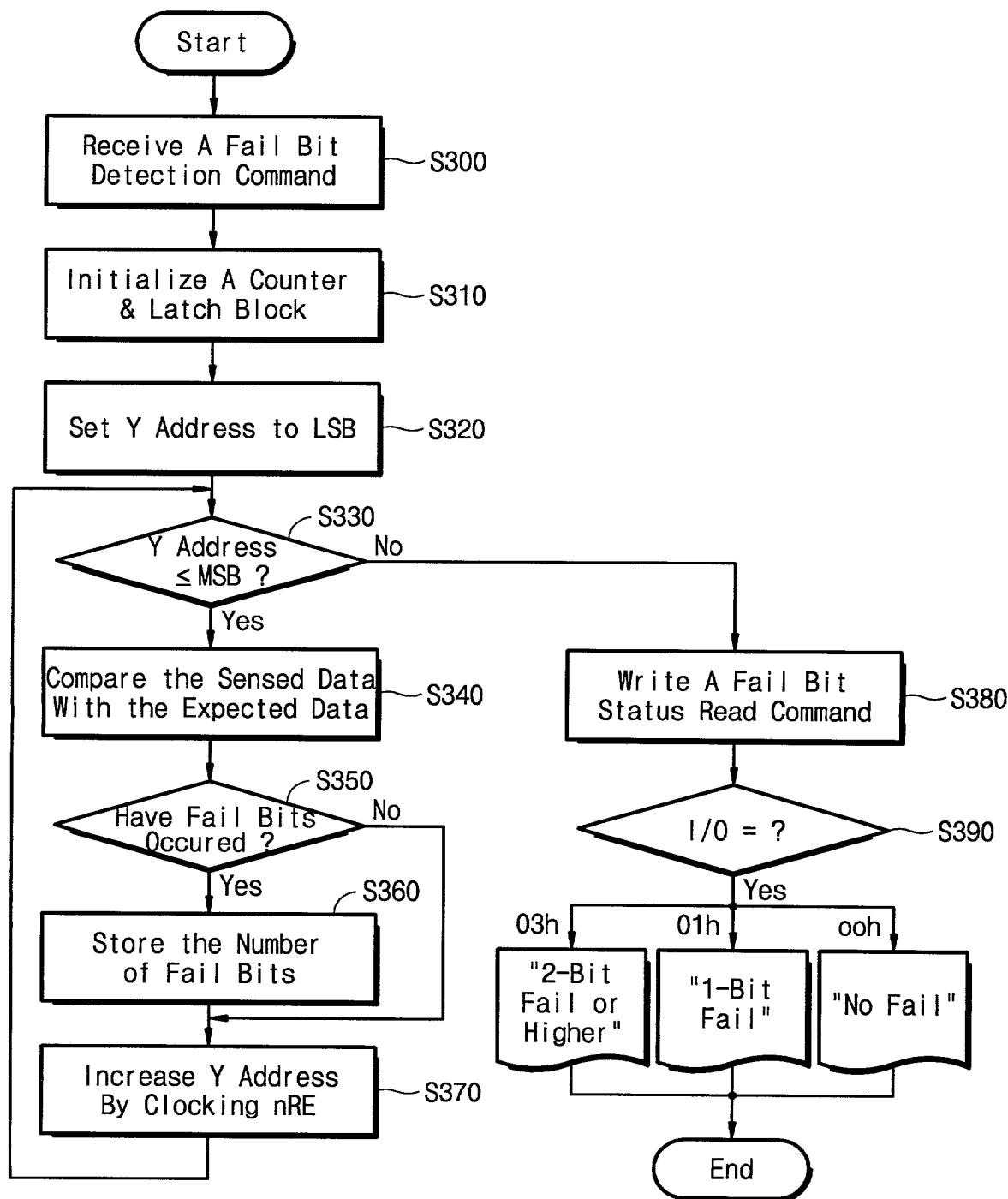


Fig. 11A

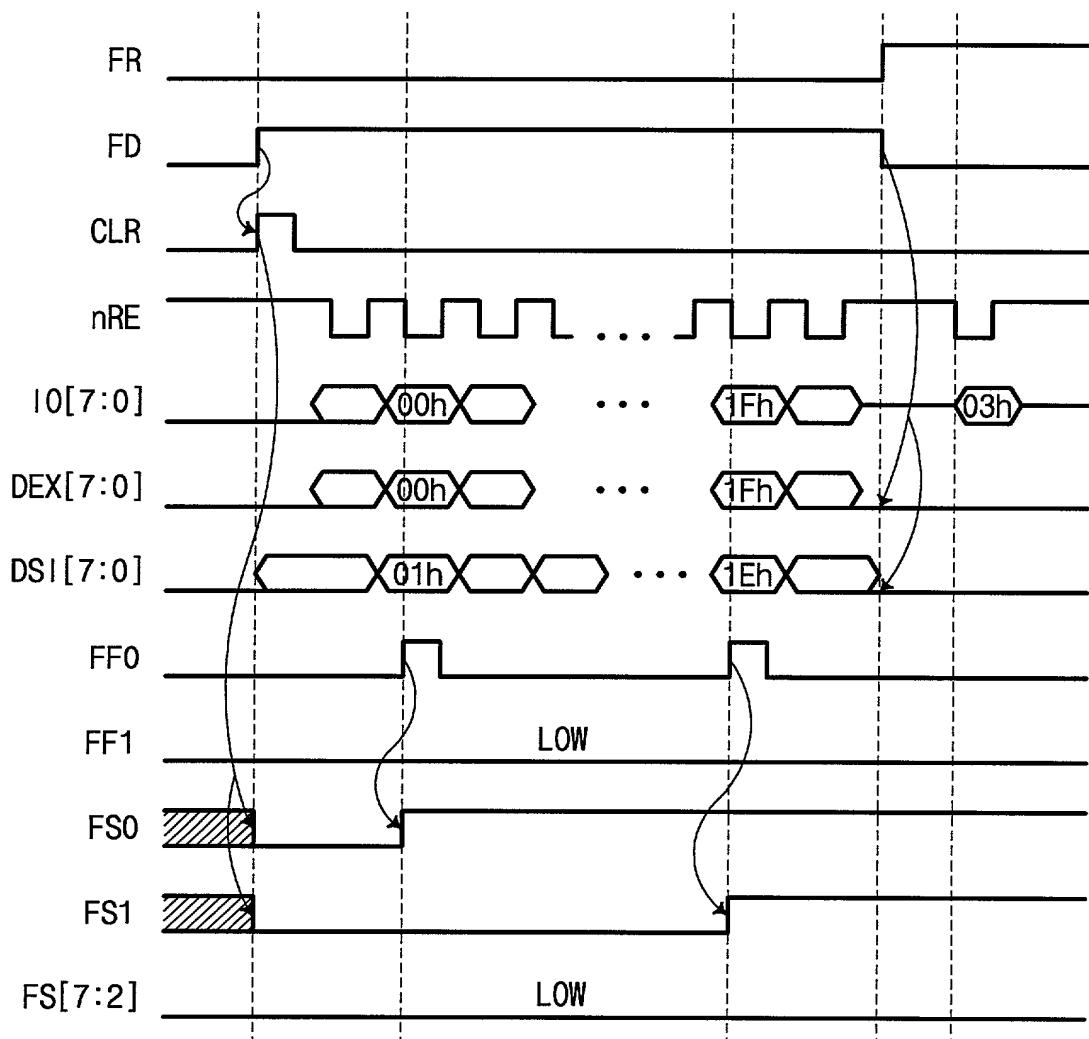


Fig. 11B

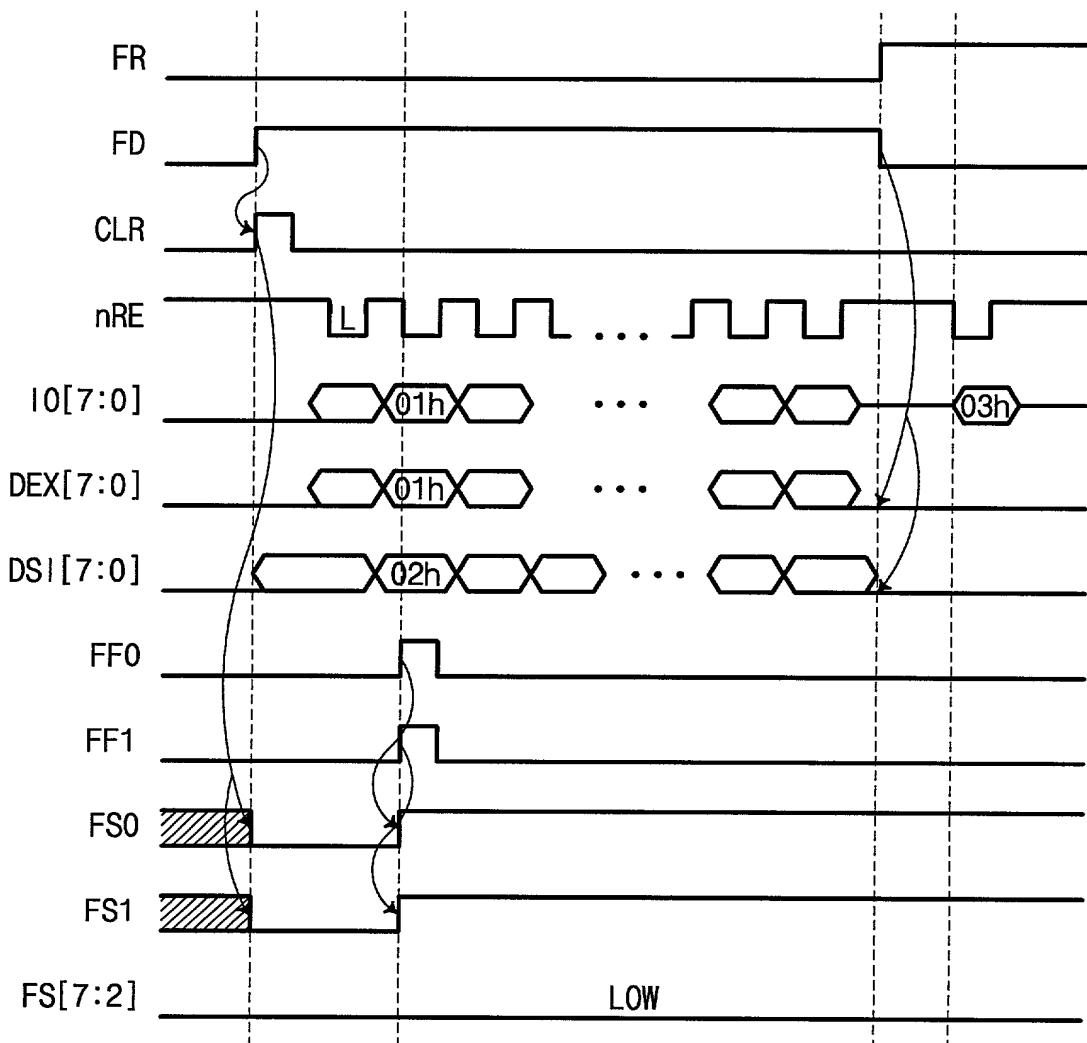


Fig. 12

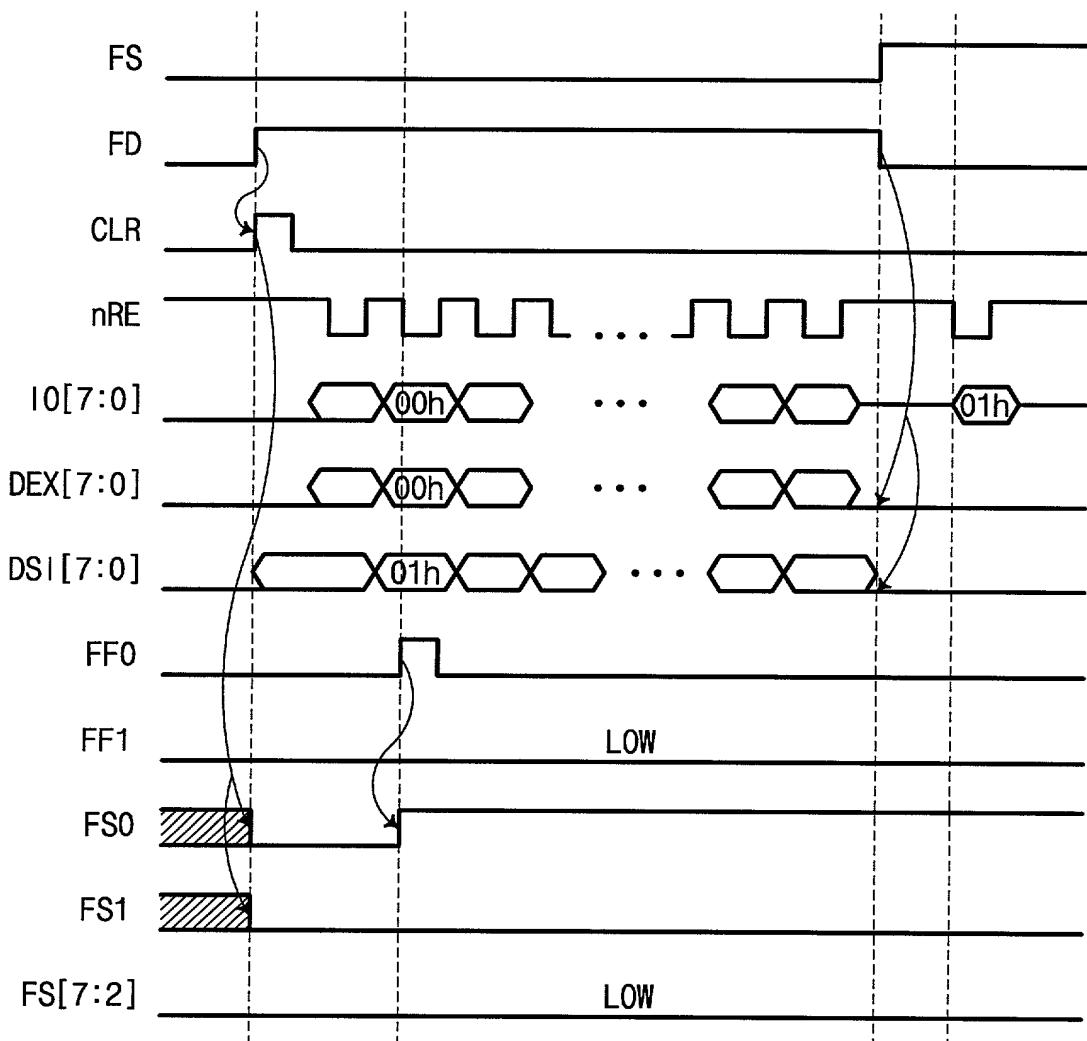


Fig. 13

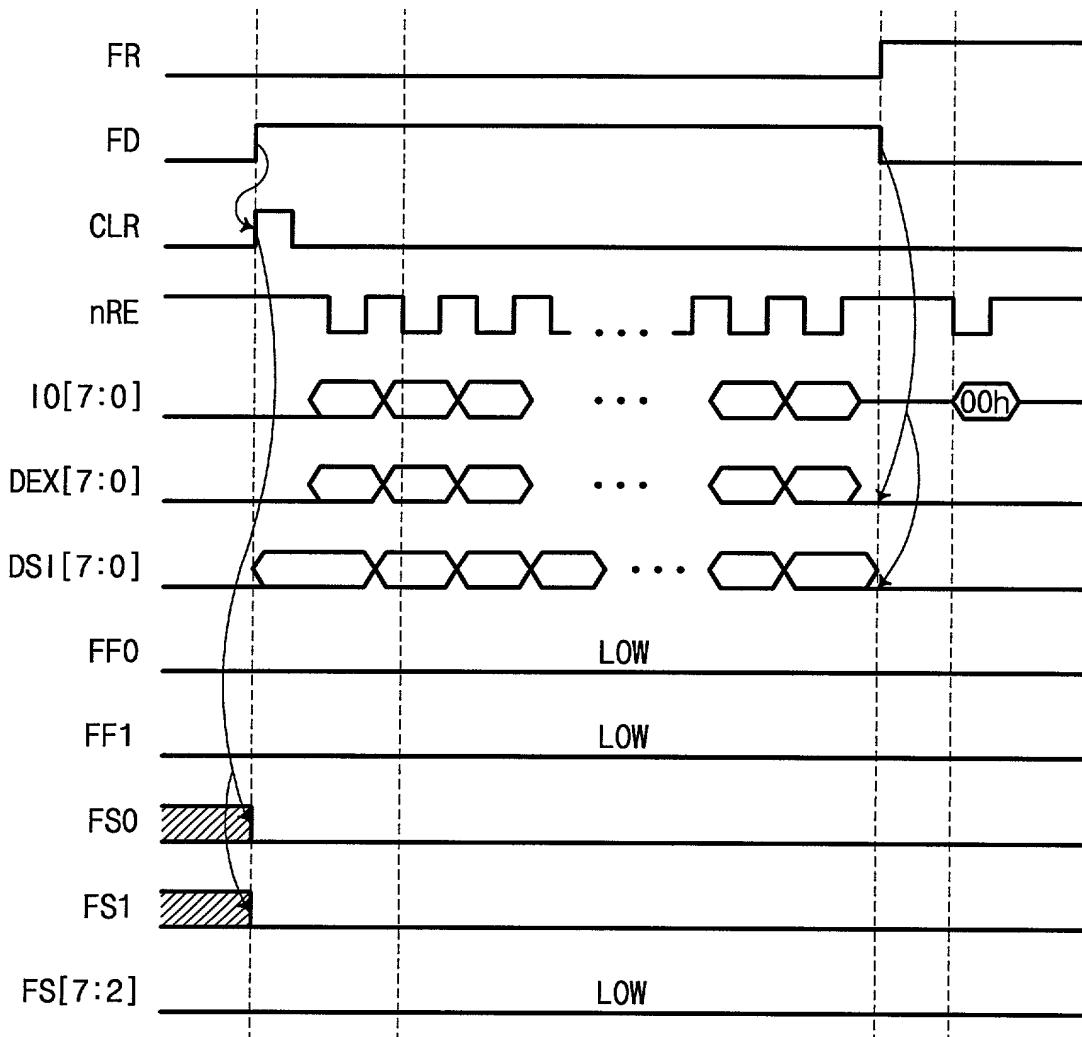


Fig. 14

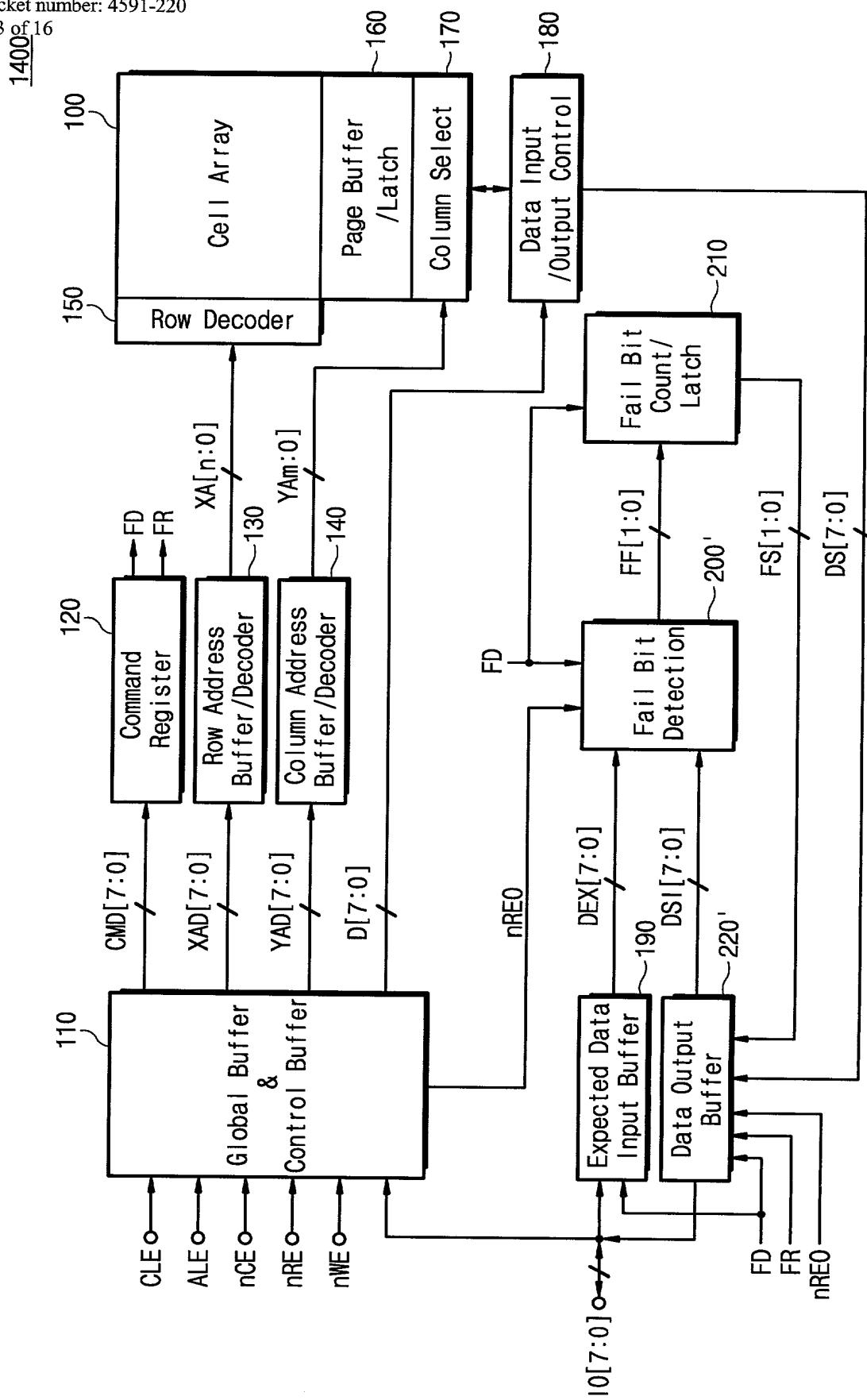


Fig. 15

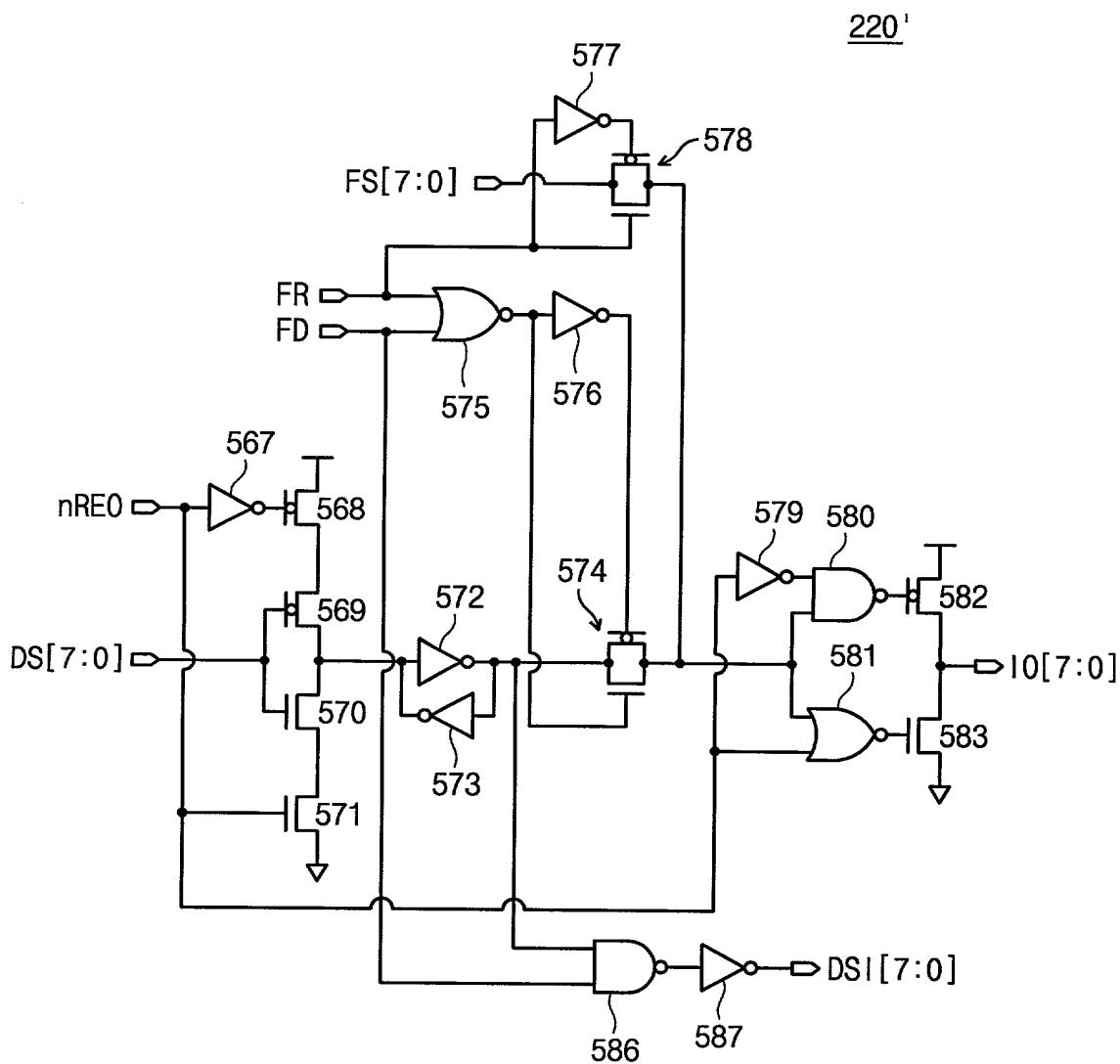


Fig. 16

200¹

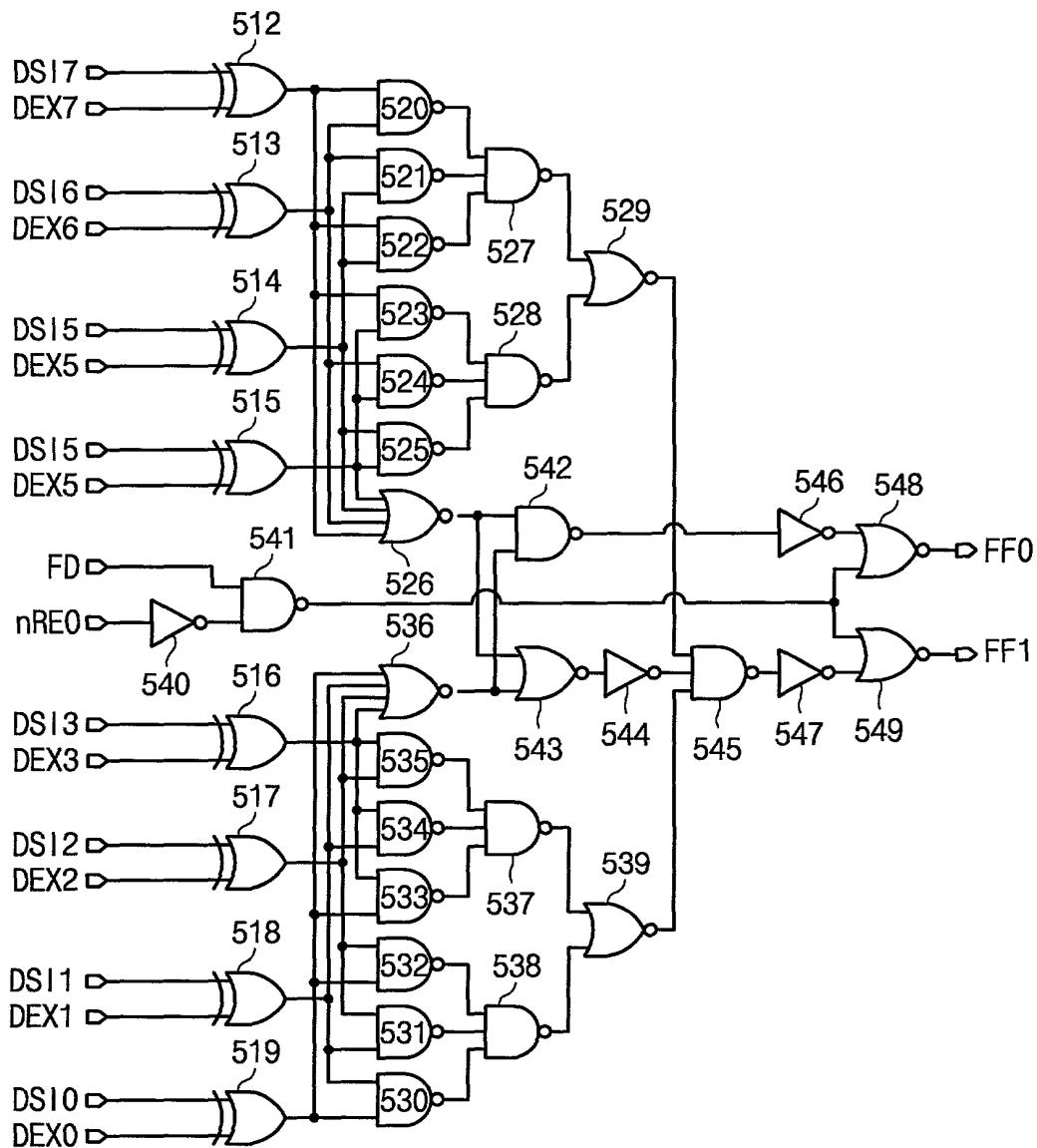


Fig. 17

1700

